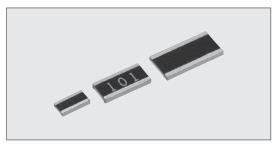
## THICK FILM (WIDE TERMINAL TYPE <ANTI SURGE>)



# **WG73** ■ Wide Terminal Type Pulse Power Flat Chip Resistors



Coating color: Wine red

## ■Features

- Superior to WK73 series in pulse withstanding voltage.
- · Suitable for flow and reflow solderings.
- Products meet EU-RoHS requirements.
- AEC-Q200 Tested.

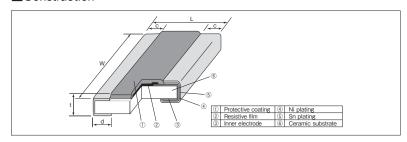
## Applications

• E.C.U.

## ■Reference Standards

IEC 60115-8 JIS C 5201-8 EIAJ RC-2134A

#### ■ Construction

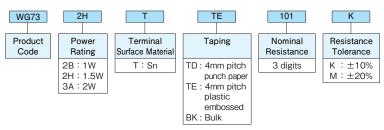


## **■**Dimensions

Туре		Weight (g)					
(Inch Size Code)	L	W	c±0.2	d±0.15	t±0.1	(1000pcs)	
2B (0612)	1.6 +0.1 -0.2	3.2 +0.1 -0.3	0.3	0.45	0.6	12.0	
2H (1020)	2.5±0.15	5.0±0.15	0.4	0.75		30.2	
3A (1225)	3.1 +0.2	6.3±0.15	0.45	0.75		45.6	

## ■Type Designation

#### Example



Contact us when you have control request for environmental hazardous material other than the substance specified by EU-RoHS.

For further information on taping, please refer to APPENDIX C on the back pages.

## Ratings

	Туре	Power Rating	Rated Ambient Temp.	Rated Terminal Part Temp.	T.C.R. (×10 <sup>-6</sup> /K)	Resistance Range (Ω) K:±10% M:±20%		Max. Working	Max. Overload	Taping & Q' ty /Reel (pcs)	
	,,,,					E12		Voltage	Voltage	TD	TE
	2B	1 W	70°C	125℃	±100	560m~1k	560m~1k	200V	400V	5,000	_
	2H	1.5W								_	4,000
Ī	3A	2W								_	4,000

Operating Temperature Range : −55°C ~+155°C

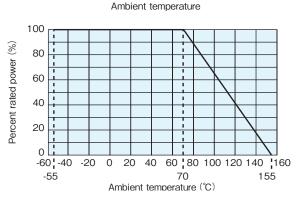
 $Rated\ voltage = \sqrt{Power\ Rating \times Resistance\ value}\ or\ Max.\ working\ voltage,\ whichever\ is\ lower.$ 

If any questions arise whether to use the "Rated Ambient Temperature" or the "Rated Terminal Part Temperature" in your usage conditions, please give priority to the "Rated Terminal Part Temperature".

For more details, please refer to "introduction of the derating curves based on the terminal part temperature" on the beginning of our catalog.



### ■ Derating Curve



For resistors operated at an ambient temperature of  $70^{\circ}\!\text{C}$  or higher, the power shall be derated in accordance with the derating curve.

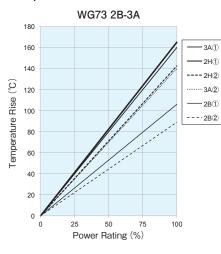
#### Terminal part temperature 100 90 Percent rated power (%) 80 70 60 50 40 30 20 10 0 -60:-40 -20 0 20 40 60 80 100 120, 140 **'**160 -55 125 155 Terminal part temperature (°C)

When the terminal part temperature of the resistor exceeds the rated terminal part temperature shown above, the power shall be derated according to the derating curve.

Please refer to "Introduction of the derating curves based on the terminal part temperature" on the

Please refer to "Introduction of the derating curves based on the terminal part temperature" on the beginning of our catalog before use.

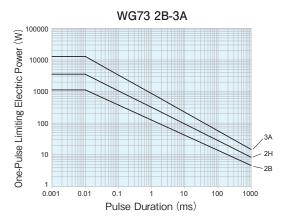
## ■Temperature Rise



Regarding the temperature rise, the value of the temperature varies per conditions and board for use since the temperature is measured under our measuring conditions.

Room temperature: 25°C
PCB: FR-4t = 1.6mm
Cu foil thickness: 35µm
①:Hot spot

## ■One-Pulse Limiting Electric Power



The maximum applicable voltage is equal to the max, overload voltage. Please ask us about the resistance characteristic of continuous applied pulse. The pulse endurance values are not assured values, so be sure to check the products on actual equipment when you use them.

## ■Performance

Took Massa	Performance Requirements 2	ΔR± (%+0.005Ω)	Took Mathada		
Test Items	Limit	Typical	Test Methods		
Resistance	Within specified tolerance	=	25℃		
T.C.R.	Within specified T.C.R.	_	+25°C/-55°C and +25°C/+125°C		
Overload (Short time)	2	0.2	Rated voltage(DC) × 2.5 for 5s		
Resistance to soldering heat	1	0.2	260°C±5°C、10s±1s		
Bending test	1	0.1	Holding point 90mm, Bending 1time. Bending 5mm		
Rapid change of temperature	2	1	-55°C (30min.) /+125°C (30min.) 1000 cycles		
Moisture resistance	2	0.2	40°C±2°C、90%~95%RH, 1000h 1.5h ON/0.5h OFF cycle		
Endurance at 70°C or rated terminal part temperature	2	0.2	70°C±2°C or rated terminal part temperature ±2°C 1000h 1.5h ON/0.5h OFF cycle		
High temperature exposure	1	0.2	+155°C、1000h		

## ■Precautions for Use

• The substrate of chip resistors is alumina. Cracks may occur at the connection of solder (solder fillet portion) due to the difference of the coefficient of thermal expansion from a mounting board when heat stress like heat cycle, etc. are repeatedly given to them. Care should be taken to the occurrence of the cracks when the change in ambient temperature or ON/OFF of load is repeated, especially when WG73 series which have self-heating. The occurrence of the crack by heat stress may be influenced by the size of a pad, solder volume, heat radiation of mounting board etc., so please pay careful attention to designing when a big change in ambient temperature and conditions for use like ON/OFF of load can be assumed.