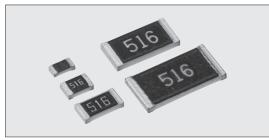
THICK FILM (FOR HIGH VOLTAGE)



HV73 Flat Chip Resistors For High Voltage



Coating color : Black

Features

- Superior to RK73 series in maximum working voltage.
- Suitable for flow and reflow solderings.
- Products meet EU-RoHS requirements. EU-RoHS regulation is not intended for Pb-glass contained in electrode, resistor element and glass.

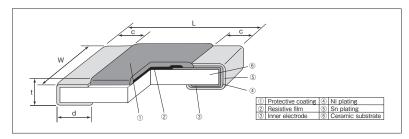
Applications

• Camera Strobe, LCD back-light, AC Adapters etc.

■Reference Standards

IEC 60115-8 JIS C 5201-8 EIAJ RC-2134C

Construction



Dimensions

Туре		Weight(g)				
(Inch Size Code)	L±0.2	W	С	d	t±0.1	(1000pcs)
1J (0603)	1.6	0.8±0.1	0.3±0.1	0.3±0.1	0.45	2.14
2A (0805)	2.0	1.25±0.1	0.4±0.2	0.3+0.2	0.5	4.54
2B (1206)	3.2	1.6±0.2				9.14
2H (2010)	5.0	2.5±0.2	0.5±0.3	0.4+0.2	0.6	24.3
3A (2512)	6.3	3.1±0.2				37.1

Type Designation

Example					
HV73	2B	T	TD	1004	F
Product Code	Power Rating	Terminal Surface Material	Taping	Nominal Resistance	Resistance Tolerance
	1J:0.1W 2A:0.25W 2B:0.25W 2H:0.5W 3A:1W	T:Sn	TD: 4mm pitch punch paper TE: 4mm pitch plastic embossed BK:Bulk	D,F:4 digits G,J:3 digits	D:±0.5% F:±1% G:±2% J:±5%

Contact us when you have control request for environmental hazardous material other than the substance specified by EU-RoHS

For further information on taping, please refer to APPENDIX C on the back pages.

Ratings

	_ Power Rated Am		ed Ambient Rated Terminal Part Temp.	T.C.R.	Resistance Range (Ω)				Max. Working	Max. Overload	Taping & Q'ty/Reel	
Type Rating	Temp.	(×10-6/K)		D:±0.5%	F:±1%	G:±2%	J:±5%		Voltage (D.C.) *1	(pcs)		
	Hatting	Tomp.	r urt romp.	(710 710	E24 · E96	E24 · E96	E24	E24	Voltago	Voltago (D.O.)	TD	TE
1J	0.1W	70℃	125℃	±100*2	_	10k~10M	10k~10M	10k~10M	350V	500V	5,000	_
2A	0.25W	70℃	125℃	±100	100k~1M	100k~10M	100k~10M	100k~10M	400V	800V	5,000	_
ZA	0.25	700	1250	±200	_	_	_	11M~51M				
2B	0.25W	70°0	125℃	±100	100k~1M	100k~10M	100k~10M	100k~10M	800V	1000V	5,000	П
26	0.25	70℃	1250	±200	_	_	_	11M~51M				
				±100	100k~1M	100k~10M	100k~10M	100k~10M				
2H	2H 0.5W 70℃	125℃	±200	_	10.2M~51M	11M~51M	11M~51M	2000V (D.C.)	3000V	_	4,000	
			±300	_	51.1M~100M	56M~100M	56M~100M					
24	3A 1W 70°C	70%	°C 125°C	±100	43k~1M	43k~10M	43k~10M	43k~10M	3000V(D.C.)	4000V	-	4,000
3A		70 C	1250	±200	_	10.2M~20M	11M~20M	11M~51M				

Operating Temperature Range : $-55^{\circ}\text{C} \sim +155^{\circ}\text{C}$

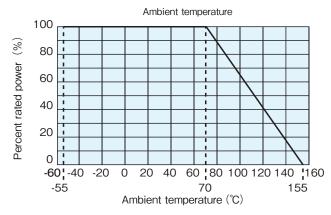
Rated voltage=√Power Rating×Resistance value or Max. working voltage, whichever is lower.

**1 Max. overload voltage is specified by D.C. voltage. **2 Cold T.C.R. (-55°C \sim +25°C) of 1.02M Ω \sim 10M Ω is \pm 200 \times 10 $^{-6}$ /K.

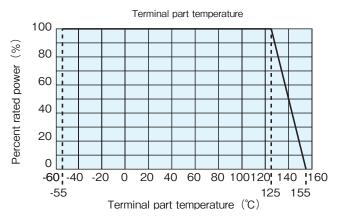
If any questions arise whether to use the "Rated Ambient Temperature" or the "Rated Terminal Part Temperature" in your usage conditions, please give priority to the "Rated Terminal Part Temperature". For more details, please refer to "Introduction of the derating curves based on the terminal part temperature" on the beginning of our catalog.



Derating Curve

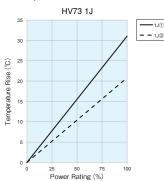


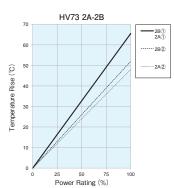
For resistors operated at an ambient temperature of 70°C or higher, the power shall be derated in accordance with the above derating curve.

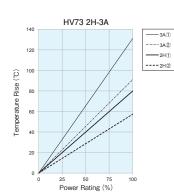


When the terminal part temperature of the resistor exceeds the rated terminal part temperature shown above, the power shall be derated according to the derating curve.
**Please refer to "Introduction of the derating curves based on the terminal part temperature" on the beginning of our catalog before use.

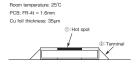
■Temperature Rise



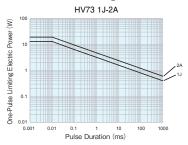


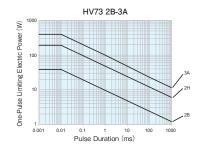


Regarding the temperature rise, the value of the temperature varies per conditions and board for use since the temperature is measured under our measuring conditions.



■One-Pulse Limiting Electric Power





The maximum applicable voltage is equal to the max. overload voltage. Please ask us about the resistance characteristic of continuous applied pulse. The pulse endurance values are not assured values, so be sure to check the products on actual equipment when you use them.

■ Performance

Test Items	Performance Requirements	$\Delta R\pm (\%+0.1\Omega)$	Test Methods		
rest items	Limit	Typical	rest wethous		
Resistance	Within specified tolerance	_	25°C		
T.C.R.	Within specified T.C.R.	_	+25°C/−55°C and +25°C/+125°C		
Overload (Short time)	2	0.5	Rated voltage (D.C.) ×2.5 for 5s		
Resistance to soldering heat	1	0.5	260°C±5°C, 10s±1s		
Rapid change of temperature	0.5:10kΩ≦R≦10MΩ 1:10MΩ <r≦100mω< td=""><td>0.3:10kΩ≦R≦10MΩ 0.5:10MΩ<r≦100mω< td=""><td>-55°C (30min.) /+125°C (30min.) 100 cycles</td></r≦100mω<></td></r≦100mω<>	0.3:10kΩ≦R≦10MΩ 0.5:10MΩ <r≦100mω< td=""><td>-55°C (30min.) /+125°C (30min.) 100 cycles</td></r≦100mω<>	-55°C (30min.) /+125°C (30min.) 100 cycles		
Moisture resistance 2 0.75		40°C±2°C, 90%~95%RH, 1000h 1.5h 0N/0.5h 0FF cycle			
Endurance at 70°C or rated terminal part temperature	2	0.75	70°C±2°C or rated terminal part temperature ±2°C 1000h 1.5h 0N/0.5h 0FF cycle		
High temperature exposure	2	0.3	+155℃, 1000h		

■Precautions for Use

- Max. overload voltage is specified by D.C. voltage. When using in A.C. voltage, the peek value of A.C. voltage shall not exceed the Maximum overload voltage.
- The substrate of chip resistors is alumina. Cracks may occur at the connection of solder (solder fillet portion) due to the difference of the coefficient of thermal expansion from a mounting board when heat stress like heat cycle, etc. are repeatedly given to them. Care should be taken to the occurrence of the cracks when the change in ambient temperature or ON/OFF of load is repeated, especially when large types of 2H/3A which have large thermal expansion and also self heating. By general temperature cycle test using glass-epoxy (FR-4) boards under the maximum/minimum temperatures of operating temperature range, the crack does not occur easily in the types of 1J~2B, but the crack tends to occur in the types of 2H/3A. The occurrence of the crack by heat stress may be influenced by the size of a pad, solder volume, heat radiation of mounting board etc., so please pay careful attention to designing when a big change in ambient temperature and conditions for use like ON/OFF of load can be assumed.